

## Homework 8

### Due Wednesday, November 9

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#### PRIVILEGE, EXCEPTIONS

**Read Ch 6**

Do Chapter 6 #1, 2, 5, 6, 7, 8, 12, 13

X1. On our 68KMB boards, ROM contains:

```
00000000  0000 C000 0000 4000 0000 0506 0000 050C
00000010  0000 0518 0000 051E 0000 0524 0000 052A
00000020  0000 0530 0000 1328 0000 0536 0000 053C
00000030  0000 0566 0000 0566 0000 0542 0000 0548
.....
```

At what address is the handler for a divide-by-zero exception?

X2. When an exception occurs, the 68000 performs the following steps:

1. Save the SR internally
2. Enter supervisor state
3. Calculate the address of the exception handler
4. Save the PC and internally saved SR onto the stack
5. Load the PC with the address from step 3.

Why does step 2 need to happen before step 4?

Why is step 1 necessary?