

Homework 2

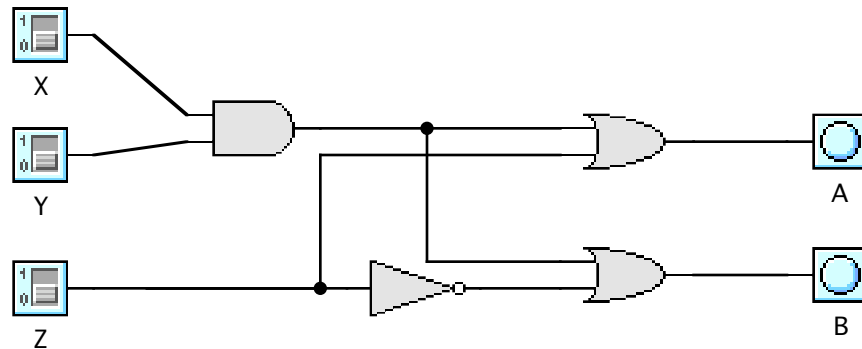
Due Wednesday, Sep. 14

CIRCUITS AND LOGIC

Read Chapter 0.4 - 0.7.

Do Chapter 0 # 12, 15, 19, 23

1. Consider the following circuit:



- Make a table which shows the outputs for all possible inputs.
2. Design a 1 bit, 2 input selector. This has three inputs A, B, and Select, and one output Z. If Select is 0 then the output $Z = A$. If Select is 1, then the output $Z = B$.
 3. Explain the operation of the SR-latch, pictured on page 31 of the textbook.